UNIVERSITY OF MUMBAI

Revised syllabus (Rev- 2016) from Academic Year 2016 -17

Under

FACULTY OF TECHNOLOGY

Computer Engineering

Second Year with Effect from **AY 2017-18**
Third Year with Effect from **AY 2018-19**
Final Year with Effect from **AY 2019-20**

As per **Choice Based Credit and Grading System**
with effect from the AY 2016–17

Co-ordinator, Faculty of Technology's Preamble:

To meet the challenge of ensuring excellence in engineering education, the issue of quality needs to be addressed, debated and taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education. The major emphasis of accreditation process is to measure the outcomes of the program that is being accredited. In line with this Faculty of Technology of University of Mumbai has taken a lead in incorporating philosophy of outcome based education in the process of curriculum development.

Faculty of Technology, University of Mumbai, in one of its meeting unanimously resolved that, each Board of Studies shall prepare some Program Educational Objectives (PEO's) and give freedom to affiliated Institutes to add few (PEO's). It is also resolved that course objectives and course outcomes are to be clearly defined for each course, so that all faculty members in affiliated institutes understand the depth and approach of course to be taught, which will enhance learner's learning process. It was also resolved that, maximum senior faculty from colleges and experts from industry to be involved while revising the curriculum. I am happy to state that, each Board of studies has adhered to the resolutions passed by Faculty of Technology, and developed curriculum accordingly. In addition to outcome based education, semester based credit and grading system is also introduced to ensure quality of engineering education.

Choice based Credit and Grading system enables a much-required shift in focus from teacher-centric to learner-centric education since the workload estimated is based on the investment of time in learning and not in teaching. It also focuses on continuous evaluation which will enhance the quality of education. University of Mumbai has taken a lead in implementing the system through its affiliated Institutes and Faculty of Technology has devised a transparent credit assignment policy and adopted ten points scale to grade learner's performance. Credit assignment for courses is based on 15 weeks teaching learning process, however content of courses is to be taught in 12-13 weeks and remaining 2-3 weeks to be utilized for revision, guest lectures, coverage of content beyond syllabus etc.

Choice based Credit and grading system is implemented from the academic year 2016-17 through optional courses at department and institute level. This will be effective for SE, TE and BE from academic year 2017-18, 2018-19 and 2019-20 respectively.

Dr. S. K. Ukarande
Co-ordinator,
Faculty of Technology,
Member - Academic Council
University of Mumbai, Mumbai

Chairman's Preamble:

Engineering education in India is expanding and is set to increase manifold. The major challenge in the current scenario is to ensure quality to the stakeholders along with expansion. To meet this challenge, the issue of quality needs to be addressed, debated and taken forward in a systematic manner. Accreditation is the principal means of quality assurance in higher education and reflects the fact that in achieving recognition, the institution or program of study is committed and open to external review to meet certain minimum specified standards. The major emphasis of this accreditation process is to measure the outcomes of the program that is being accredited. Program outcomes are essentially a range of skills and knowledge that a student will have at the time of graduation from the program. In line with this Faculty of Technology of University of Mumbai has taken a lead in incorporating the philosophy of outcome based education in the process of curriculum development.

As the Chairman, Board of Studies in Computer Engineering of the University of Mumbai, I am happy to state here that, the Program Educational Objectives for Undergraduate Program were finalized in a brain storming session, which was attended by more than 85 members from different affiliated Institutes of the University. They are either Heads of Departments or their senior representatives from the Department of Computer Engineering. The Program Educational Objectives finalized for the undergraduate program in Computer Engineering are listed below;

1. To prepare the Learner with a sound foundation in the mathematical, scientific and engineering fundamentals.
2. To motivate the Learner in the art of self-learning and to use modern tools for solving real life problems.
3. To equip the Learner with broad education necessary to understand the impact of Computer Science and Engineering in a global and social context.
4. To encourage, motivate and prepare the Learner's for Lifelong- learning.
5. To inculcate professional and ethical attitude, good leadership qualities and commitment to social responsibilities in the Learner's thought process.

In addition to Program Educational Objectives, for each course of the program, objectives and expected outcomes from a learner's point of view are also included in the curriculum to support the philosophy of outcome based education. I strongly believe that even a small step taken in the right direction will definitely help in providing quality education to the major stakeholders.

Dr. Subhash K. Shinde
Chairman, Board of Studies in Computer Engineering,
University of Mumbai, Mumbai.

Course Code	Course Name	Teaching Scheme (Contact Hours)			Credits Assigned			
		Theory	Pract	Tut	Theory	TW/ Pract	Tut	Total
CSC501	Microprocessor	4	-	-	4	-	-	4
CSC502	Database Management System	4	-	-	4	-	-	4
CSC503	Computer Network	4	-	-	4	-	-	4
CSC504	Theory of Computer Science	3+1@	-	-	4	-	-	4
CSDLO 501X	Department Level Optional Course -I	4	-	-	4	-	-	4
CSL501	Microprocessor Lab	-	2	-	-	1		1
CSL502	Computer Network Lab	-	2	-	-	1	-	1
CSL503	Database & Info. System Lab	-	2	-	-	1	-	1
CSL504	Web Design Lab	-	2+2*	-	-	2	-	2
CSL505	Business Comm. & Ethics	-	2+2*	-	-	2	-	2
	Total	**20**	**14**	**-**	**20**	**7**	**-**	**27**

@ 1 hour to be taken tutorial as class wise.
*2 hours shown as Practical's to be taken class wise and other 2 hours to be taken as batch wise

Course Code	Course Name	Examination Scheme							
		Theory					TW	Oral & Pract	Total
		Internal Assessment			End Sem. Exam	Exam Duration (in Hrs)			
		Test 1	Test 2	Avg.					
CSC501	Microprocessor	20	20	20	80	3	-	-	100
CSC502	Database Management System	20	20	20	80	3	-	-	100
CSC503	Computer Network	20	20	20	80	3	-	-	100
CSC504	Theory of Computer Science	20	20	20	80	3	-	-	100
CSDLO 501X	Department Level Optional Course -I	20	20	20	80	3	--	-	100
CSL501	Microprocessor Lab	-	-	-	-	-	25	25	50
CSL502	Computer Network Lab	-	-	-	-	-	25	25	50
CSL503	Database & Info. System Lab	-	-	-	-	-	25	25	50
CSL504	Web Design Lab	-	-	-	-	-	25	25	50
CSL505	Business Comm. & Ethics	-	-	-	-	-	50	-	50
	Total	100	100	100	400	-	150	100	750

Sem.	Department Level Optional Course (DLOC)	Institute Level Optional Course (ILOC)
V	CSDLO5011: Multimedia System CSDLO5012: Advance Operating System CSDLO5013: Advance Algorithm	--------------------
VI	CSDLO6021: Machine Learning CSDLO6022: Advance Database System CSDLO6023: Enterprise Resource Planning CSDLO6024: Advance Computer Network	-------------------
VII	CSDLO7031: Advance System Security & Digital Forensics CSDLO7032: Big Data & Analytics CSDLO7033: Robotics	ILO7011. Product Lifecycle Management ILO7012. Reliability Engineering ILO7013. Management Information System ILO7014. Design of Experiments ILO7015. Operation Research ILO7016. Cyber Security and Laws ILO7017. Disaster Management & Mitigation Measures ILO7018. Energy Audit and Management ILO7019. Development Engineering
VIII	DLO8011: High Performance Computing DLO8012: Natural Language Processing DLO8013: Adhoc Wireless Network	ILO8021. Project Management ILO8022. Finance Management ILO8023. Entrepreneurship Development and Management ILO8024. Human Resource Management ILO8025. Professional Ethics and CSR ILO8026. Research Methodology ILO8027. IPR and Patenting ILO8028. Digital Business Management ILO8029. Environmental Management

Course Code	Course Name	Credits
CSC501	Microprocessor	4

Course objectives:
1. To equip students with the fundamental knowledge and basic technical competence in the field of Microprocessors.
2. To emphasize on instruction set and logic to build assembly language programs.
3. To prepare students for higher processor architectures and Embedded systems

Course outcomes: On successful completion of course learner will be able to:
1. Describe architecture of x86 processors.
2. Interpret the instructions of 8086 and write assembly and Mixed language programs.
3. Explain the concept of interrupts
4. Identify the specifications of peripheral chip
5. Design 8086 based system using memory and peripheral chips
6. Appraise the architecture of advanced processors

Prerequisite: Digital Electronics and Logic Design

Module No.	Unit No.	Topics	Hrs.
1.0		**The Intel Microprocessors 8086/8088 Architecture**	10
	1.1	• 8086/8088 CPU Architecture, Programmer's Model • Functional Pin Diagram • Memory Segmentation • Banking in 8086 • Demultiplexing of Address/Data bus • Study of 8284 Clock Generator • Study of 8288 Bus Controller • Functioning of 8086 in Minimum mode and Maximum mode • Timing diagrams for Read and Write operations in minimum and maximum mode	
2.0		**Instruction Set and Programming**	12
	2.1	• Addressing Modes • Instruction set – Data Transfer Instructions, String Instructions, Logical Instructions, Arithmetic Instructions, Transfer of Control Instructions, Processor Control Instructions • Assembler Directives and Assembly Language Programming, Macros, Procedures • Mixed Language Programming with C Language and Assembly Language. • Programming based on DOS and BIOS Interrupts (INT 21H, INT 10H)	
3.0		**8086 Interrupts**	6
	3.1	• Types of interrupts • Interrupt Service Routine • Interrupt Vector Table • Servicing of Interrupts by 8086 microprocessor • Programmable Interrupt Controller 8259 – Block Diagram, Interfacing the 8259 in single and cascaded mode, Operating modes, programs for 8259 using ICWs and OCWs	

4.0		**Peripherals and their interfacing with 8086**	12
	4.1	Memory Interfacing - RAM and ROM Decoding Techniques – Partial and Absolute	
	4.2	8255-PPI – Block diagram, Functional PIN Diagram, CWR, operating modes, interfacing with 8086.	
	4.3	8253 PIT - Block diagram, Functional PIN Diagram, CWR, operating modes, interfacing with 8086.	
	4.4	8257-DMAC – Block diagram, Functional PIN Diagram, Register organization, DMA operations and transfer modes	
5.0		**Intel 80386DX Processor**	6
	5.1	• Architecture of 80386 microprocessor • 80386 registers – General purpose Registers, EFLAGS and Control registers • Real mode, Protected mode, virtual 8086 mode • 80386 memory management in Protected Mode – Descriptors and selectors, descriptor tables, the memory paging mechanism	
6.0		**Pentium Processor**	6
	6.1	Pentium Architecture Superscalar Operation, Integer & Floating Point Pipeline Stages, Branch Prediction Logic, Cache Organisation and MESI Model	
		Total	52

Assessment:

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approx. 40% syllabus is completed and second class test when additional 40% syllabus is completed. Duration of each test shall be one hour.

End Semester Theory Examination:
1 Question paper will comprise of 6 questions, each carrying 20 marks.
1. The students need to solve total 4 questions.
2. Question No.1 will be compulsory and based on entire syllabus.
3. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Text Books:
1. 8086/8088 family: Design Programming and Interfacing: John Uffenbeck , PHI.
2. Advanced Microprocessors and Peripherals: K M Bhurchandani, A k Ray McGraw Hill
3. The 80386DX Microprocessor: hardware, Software and Interfacing, Walter A Triebel, Prentice Hall
4. Pentium Processor System Architecture: Tom Shanley & Don Anderson, Addison-Wesley.

Reference Books:

1. Intel Microprocessors: Barry B. Brey, 8th Edition, Pearson Education India
2. Microprocessor and Interfacing: Douglas Hall, Tata McGraw Hill.
3. Advanced MS DOS Programming – Ray Duncan BPB
4. Intel 80386 Datasheets
5. IBM PC Assembly language and Programming: Peter Abel, 5th edition, PHI
6. The Pentium Microprocessor, James Antonakons, Pearson Education

Course Code	Course Name	Credits
CSC502	**Database Management System**	**4**

Course objectives:

1. Learn and practice data modelling using the entity-relationship and developing database designs.
2. Understand the use of Structured Query Language (SQL) and learn SQL syntax.
3. Apply normalization techniques to normalize the database
4. Understand the needs of database processing and learn techniques for controlling the consequences of concurrent data access.

Course outcomes: On successful completion of course learner will be able to:

1. Understand the fundamentals of a database systems
2. Design and draw ER and EER diagram for the real life problem.
3. Convert conceptual model to relational model and formulate relational algebra queries.
4. Design and querying database using SQL.
5. Analyze and apply concepts of normalization to relational database design.
6. Understand the concept of transaction, concurrency and recovery.

Prerequisite:
Basic knowledge of Data structure.

Module No.	Unit No.	Topics	Hrs.
1.0		**Introduction Database Concepts:**	**4**
	1.1	● Introduction, Characteristics of databases ● File system v/s Database system ● Users of Database system	
	1.2	● Data Independence ● DBMS system architecture ● Database Administrator	
2.0		**Entity–Relationship Data Model**	**8**
	2.1	● The Entity-Relationship (ER) Model: Entity types : Weak and strong entity sets, Entity sets, Types of Attributes, Keys, Relationship constraints : Cardinality and Participation, Extended Entity-Relationship (EER) Model : Generalization, Specialization and Aggregation	
3.0		**Relational Model and relational Algebra**	**8**
	3.1	● Introduction to the Relational Model, relational schema and concept of keys. ● Mapping the ER and EER Model to the Relational Model	
	3.2	● Relational Algebra – unary and set operations, Relational Algebra Queries.	
4.0		**Structured Query Language (SQL)**	**12**
	4.1	● Overview of SQL	

		● Data Definition Commands, Data Manipulation commands, Data Control commands, Transaction Control Commands.	
	4.2	● Set and string operations, aggregate function - group by, having. ● Views in SQL, joins , Nested and complex queries, Integrity constraints :- key constraints, Domain Constraints, Referential integrity , check constraints	
	4.3	● Triggers	
5.0		**Relational–Database Design**	**8**
	5.1	● Pitfalls in Relational-Database designs , Concept of normalization ● Function Dependencies , First Normal Form, 2nd , 3rd , BCNF, multi valued dependencies , 4NF.	
6.0		**Transactions Management and Concurrency**	**12**
	6.1	● Transaction concept, Transaction states, ACID properties ● Concurrent Executions, Serializability – Conflict and View, Concurrency Control: Lock-based, Timestamp-based protocols.	
	6.2	● Recovery System: Failure Classification, Log based recovery, ARIES, Checkpoint, Shadow paging. ● Deadlock handling	
		Total	**52**

Assessment:

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approx. 40% syllabus is completed and second class test when additional 40% syllabus is completed. Duration of each test shall be one hour.

End Semester Theory Examination:
1 Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Text Books:
1. G. K. Gupta "Database Management Systems", McGraw – Hill.
2. Korth, Slberchatz,Sudarshan, "Database System Concepts", 6th Edition, McGraw – Hill
3. Elmasri and Navathe, "Fundamentals of Database Systems", 5th Edition, Pearson education.
4. Peter Rob and Carlos Coronel, "Database Systems Design, Implementation and Management", Thomson Learning, 5th Edition.

Reference Books:
1. Dr. P.S. Deshpande, SQL and PL/SQL for Oracle 10g, Black Book, Dreamtech Press.
2. Gillenson, Paulraj Ponniah, "Introduction to Database Management", Wiley Publication.
3. Sharaman Shah, "Oracle for Professional", SPD.
4. Raghu Ramkrishnan and Johannes Gehrke, " Database Management Systems ",TMH.

Course Code	Course Name	Credits
CSC 503	**Computer Network**	**4**

Course objective:
1. To introduce concepts and fundamentals of data communication and computer networks.
2. To explore the inter-working of various layers of OSI.
3. To explore the issues and challenges of protocols design while delving into TCP/IP protocol suite.
4. To assess the strengths and weaknesses of various routing algorithms.
5. To understand the transport layer and various application layer protocols.

Course Outcomes:

On successful completion of course learner will be able to:
1. Demonstrate the concepts of data communication at physical layer and compare ISO - OSI model with TCP/IP model.
2. Demonstrate the knowledge of networking protocols at data link layer.
3. Design the network using IP addressing and subnetting / supernetting schemes.
4. Analyze various routing algorithms and protocols at network layer.
5. Analyze transport layer protocols and congestion control algorithms.
6. Explore protocols at application layer .

Prerequisite: Digital Communication Fundamentals

Module No.	Unit No.	Topics	Hrs.
1		**Introduction to Networking**	06
	1.1	Introduction to computer network, network application, network software and hardware components (Interconnection networking devices), Network topology, protocol hierarchies, design issues for the layers, connection oriented and connectionless services	
	1.2	Reference models: Layer details of OSI, TCP/IP models. Communication between layer.	
2		**Physical Layer**	06
	2.1	Introduction to Communication System, digital Communication, Electromagnetic Spectrum	
	2.2	**Guided Transmission Media:** Twisted pair, Coaxial, Fiber optics. Unguided media (Wireless Transmission): Radio Waves, Microwave, Bluetooth, Infrared, Circuit and Packet Switching	

3	Data Link Layer		10
	3.1	DLL Design Issues (Services, Framing, Error Control, Flow Control), Error Detection and Correction(Hamming Code, CRC, Checksum) , Elementary Data Link protocols , Stop and Wait, Sliding Window(Go Back N, Selective Repeat), HDLC	
	3.2	**Medium Access Control sublayer** Channel Allocation problem, Multiple access Protocol(Aloha, Carrier Sense Multiple Access (CSMA/CD), Local Area Networks - Ethernet (802.3)	
4	Network layer		14
	4.1	4.1 Network Layer design issues, Communication Primitives: Unicast, Multicast, Broadcast. IPv4 Addressing (classfull and classless), Subnetting, Supernetting design problems ,IPv4 Protocol, Network Address Translation (NAT)	
	4.2	**Routing algorithms :** Shortest Path (Dijkastra's), Link state routing, Distance Vector Routing	
	4.3	**Protocols** - ARP,RARP, ICMP, IGMP	
	4.4	**Congestion control algorithms:** Open loop congestion control, Closed loop congestion control, QoS parameters, Token & Leaky bucket algorithms	
5	Transport Layer		10
	5.1	**The Transport Service**: Transport service primitives, Berkeley Sockets, Connection management (Handshake), UDP, TCP, TCP state transition, TCP timers	
	5.2	TCP Flow control (sliding Window), TCP Congestion Control: Slow Start	
6	Application Layer		06
	6.1	DNS: Name Space, Resource Record and Types of Name Server. HTTP, SMTP, Telnet, FTP, DHCP	

Assessment:

Internal Assessment:

Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approx. 40% syllabus is completed and second class test when additional 40% syllabus is completed. Duration of each test shall be one hour.

End Semester Theory Examination:

1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining questions (Q.2 to Q.6) will be selected from all the modules.

Textbooks:

1. A.S. Tanenbaum, "Computer Networks", Pearson Education, (4e)
2. B.A. Forouzan, "Data Communications and Networking", TMH (5e)
3. James F. Kurose, Keith W. Ross, "Computer Networking, A Top-Down Approach Featuring the Internet", Addison Wesley, (6e)

References:

1. S.Keshav: An Engineering Approach To Computer Networking, Pearson
2. Natalia Olifer& Victor Olifer,"Computer Networks:Principles, Technologies & Protocols for Network Design", Wiley India, 2011.
3. Larry L.Peterson, Bruce S.Davie, Computer Networks: A Systems Approach, Second Edition (The Morgan Kaufmann Series in Networking).

Course Code	Course Name	Credits
CSC504	**Theory of Computer Science**	4

Course Objectives:
1. Acquire conceptual understanding of fundamentals of grammars and languages.
2. Build concepts of theoretical design of deterministic and non-deterministic finite automata and push down automata.
3. Develop understanding of different types of Turing machines and applications.
4. Understand the concept of Undecidability.

Course Outcomes: On successful completion of course learner will be able to:
1. Identify the central concepts in theory of computation and differentiate between deterministic and nondeterministic automata, also obtain equivalence of NFA and DFA.
2. Infer the equivalence of languages described by finite automata and regular expressions.
3. Devise regular, context free grammars while recognizing the strings and tokens.
4. Design pushdown automata to recognize the language.
5. Develop an understanding of computation through Turing Machine.
6. Acquire fundamental understanding of decidability and undecidability.

Prerequisite: Discrete Mathematics

Module No.	Unit No.	Topics	Theory Hrs.	Tutorial Hrs.
1.0		**Basic Concepts and Finite Automata**	**09**	**03**
	1.1	• Alphabets, Strings, Languages, Closure properties. • Finite Automata (FA) and Finite State machine (FSM).		
	1.2	• Deterministic Finite Automata (DFA) and Nondeterministic Finite Automata (NFA): Definitions, transition diagrams and Language recognizers • NFA to DFA Conversion • Equivalence between NFA with and without ε- transitions • Minimization of DFA • FSM with output: Moore and Mealy machines, Equivalence • Applications and limitations of FA		
2.0		**Regular Expressions and Languages**	**06**	**02**
	2.1	• Regular Expression (RE) • Equivalence of RE and FA, Arden's Theorem • RE Applications		
	2.2	• Regular Language (RL) • Closure properties of RLs • Decision properties of RLs • Pumping lemma for RLs		
3.0		**Grammars**	**08**	**03**
	3.1	• Grammars and Chomsky hierarchy		
	3.2	• Regular Grammar (RG)		

		• Equivalence of Left and Right linear grammar • Equivalence of RG and FA		
	3.3	**Context Free Grammars (CFG)** • Definition, Sentential forms, Leftmost and Rightmost derivations, Parse tree, Ambiguity. • Simplification and Applications. • Normal Forms: Chomsky Normal Forms (CNF) and Greibach Normal Forms (GNF). • CFLs - Pumping lemma, Closure properties		
4.0		**Pushdown Automata(PDA)**	**04**	**01**
	4.1	• Definition, Transitions ,Language of PDA • Language acceptance by final state and empty stack • PDA as generator, decider and acceptor of CFG. • Deterministic PDA , Non-Deterministic PDA • Application of PDA.		
5.0		**Turing Machine (TM)**	**09**	**03**
	5.1	• Definition, Transitions • Design of TM as generator, decider and acceptor. • Variants of TM: Multitrack, Multitape • Universal TM. • Equivalence of Single and Multi Tape TMs. • Applications, Power and Limitations of TMs. • Context Sensitivity and Linear Bound Automata.		
6.0		**Undecidability**	**03**	**01**
	6.1	• Decidability and Undecidability, • Recursive and Recursively Enumerable Languages. • Halting Problem, • Rice's Theorem, • Post Correspondence Problem,		
		Total	**39**	**13**

Assessment:

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approx. 40% syllabus is completed and second class test when additional 40% syllabus is completed. Duration of each test shall be one hour.

End Semester Theory Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Text Books:

1. John E. Hopcroft, Rajeev Motwani, Jeffery D. Ullman, "Introduction to Automata Theory, Languages andComputation", Pearson Education.
2. Michael Sipser, "Theory of Computation", Cengage learning.
3. Vivek Kulkarni, "Theory of Computation", Oxford University Press, India.

Reference Books:

1. J. C. Martin, "Introduction to Languages and the Theory of Computation", Tata McGraw Hill.
2. Kavi Mahesh, "Theory of Computation: A Problem Solving Approach", Wiley-India.

Course Code	Course Name	Credits
CSDLO5011	**Multimedia System**	**4**

Course objectives:
1. To introduce students about basic fundamentals and key aspects of Multimedia system.
2. To provide knowledge of compression techniques of different multimedia components
3. To help students to understand multimedia communication standards along with technology environment
4. To provide an opportunity to gain hands-on experience in building multimedia applications.

Course outcomes: Learner will be able to
1. To identify basics of multimedia and multimedia system architecture.
2. To understand different multimedia components.
3. To explain file formats for different multimedia components.
4. To analyze the different compression algorithms.
5. To describe various multimedia communication techniques.
6. To apply different security techniques in multimedia environment.

Prerequisite: Computer Fundaments and Graphics.

Module No.	Unit No.	Topics	Hrs.
1		**Introduction to Multimedia**	8
	1.1	Overview	
	1.2	Objects and Elements of Multimedia	
	1.3	Applications of Multimedia	
	1.4	Multimedia Systems Architecture – IMA, Workstation, Network	
	1.5	Types of Medium (Perception, Representation-..)	
	1.6	Interaction Techniques	
	1.7	I/O devices - Salient features (Electronic Pen , Scanner, Digital Camera, Printers, plotters), Storage Media (Jukebox, DVD), Multimedia Databases	
2		**Text & Digital Image**	10
		Text	
	2.1	Visual Representation, Digital Representation.	
	2.2	File Formats: RTF, TIFF.	
	2.3	Compression Techniques : Huffman Coding, RLE, CCITT group 3 1D	

		Digital Image	
	2.4	Digital Image Representation (2D format, resolution) Types of Images (monochrome, gray, color), examples of images (X-Ray, fractal, synthetic, acoustic).	
	2.5	File formats: BMP, JPG	
	2.6	Compression Techniques: fundamentals (coding, interpixel and psychovisual redundancies),Types – lossless and lossy, Lossless Compression Algorithms– Shannon-Fano, CCITT group 4 2D, Lossy Compression Algorithm – JPEG	
3		**Digital Audio**	8
	3.1	Basic Sound Concepts: computer representation of sound,	
	3.2	File Formats – WAV, MPEG Audio	
	3.3	Compression: PCM, DM, DPCM	
4		**Digital Video**	8
	4.1	Digitization of Video, types of video signals (component, composite and S-video),	
	4.2	File Formats: MPEG Video, H.261	
	4.3	Compression: MPEG	
5		**Multimedia Network Communication and Representation**	10
	5.1	Quality of Service	
	5.2	Multimedia over IP (RTP, RTSP, RTCP,RSVP)	
	5.3	Representation- Authoring systems and user interface	
6		**Multimedia Security**	8
	6.1	Requirements and properties	
	6.2	Mechanisms – Digital Signatures, Steganographic methods	
	6.3	Sample applications – unidirectional distributed systems, information systems and conference systems	
		Total	**52**

Text Books:
1. Multimedia System Design, Prabhat K. Andleigh& Kiran Thakrar, PHI.
2. Multimedia Communication Systems: Techniques, Standards & Networks, K. R. Rao, Zoran S. Bojkovic&Dragorad A. Milovanovic, TMH.
3. Multimedia Systems, K. Buford, PHI.
4. Fundamentals of Multimedia, Ze-Nian Li & Mark S. Drew, PHI.

Reference Books:
1. Multimedia Computing Communications & Applications, Ralf Steinmetz & Klara Nahrstedt, Pearson.
2. Digital Image processing, Rafael C. Gonzalez, Richard E. Woods, Pearson.
3. Multimedia Applications, Ralf Steinmetz & Klara Nahrstedt, Springer International Edition

Internal Assessment:
Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approx. 40% syllabus is completed and second class test when additional 40% syllabus is completed. Duration of each test shall be one hour.

End Semester Theory Examination:
1. Question paper will comprise of 6 questions, each carrying 20 marks.
2. The students need to solve total 4 questions.
3. Question No.1 will be compulsory and based on entire syllabus.
4. Remaining question (Q.2 to Q.6) will be selected from all the modules.

Suggested List of Experiments:
1. Create a new file format to store a multimedia data.
2. Implement a compression technique and check the efficiency on different inputs.
3. To develop a theme based multimedia presentation
4. To add a digital signature onto a document
5. To perform steganography of text onto an image and check the efficiency with different inputs.

** Perform laboratory work of this course in 'CSL504: Web Design Lab' as experiments or mini project.

Course Code	Course Name	Credits
CSDLO5012	**Advanced Operating Systems**	4

Course Objectives:

1. To understand design issues of Advanced Operating systems.
2. To understand the architecture, kernel and file management of Unix operating system.
3. To understand basic concepts and need of Distributed operating systems.
4. To understand concepts and working of different advanced Operating systems like Multiprocessor OS, Real time OS, Mobile OS.

Course Outcomes: On successful completion of the course student should be able to

1. Demonstrate understanding of design issues of Advanced operating systems and compare different types of operating systems.
2. Analyse design aspects and data structures used for file subsystem, memory subsystem and process subsystem of Unix OS.
3. Demonstrate understanding of different architectures used in Multiprocessor OS and analyse the design and data structures used in Multiprocessor operating systems.
4. Differentiate between threads and processes and compare different processor scheduling algorithms used in Multiprocessor OS
5. Classify Real Time OS and analyse various real time scheduling algorithms.
6. Explore architectures and design issues of Mobile OS,Virtual OS, Cloud OS.

Prerequisite: Operating Systems

Module	Unit	Detailed Content	Hrs
1		Introduction	04
		Functions of operating systems, Design approaches: layered, kernel based and virtual machine approach, types of advanced operating systems (NOS, DOS, Multiprocessor OS, Mobile OS, RTOS, Cloud OS)	
2		Unix Kernel and File Management	14
	2.1	System Structure, User Perspective, Architecture of Unix Operating System	
	2.2	Buffer cache: Header, Buffer Pool, Retrieving, Reading and Writing Buffer	
	2.3	File Representation: inodes: Structure of file Directories, Path conversion to inode, superblock, inode assignment, allocation of disk blocks	
3		Unix Process and Memory management	12
	3.1	Detailed design of Process Structure: Kernel Data structures for process, Structure of Uarea and Process table, Process states and Transitions	
	3.2	Context of a Process: Static and Dynamic area of context, Saving the Context Layout of System Memory, Regions, Mapping regions	

		with Process, page table and mapping virtual address to physical address.	
4		Distributed Operating system concepts	06
		Goals, Distributed Computing Models, Hardware Concepts, Software Concepts, Architecture of DOS. Design Issues: Transparency, Flexibility, Scalability, Reliability, Performance, fault tolerance	
5		Multiprocessor Operating System	08
	5.1	Introduction, Basic multiprocessor system architectures, design issues, Threads, Process synchronization: the test and set instruction, the swap instruction, implementation of the process wait	
	5.2	Processor scheduling: Issues, Co-scheduling, Smart scheduling, Affinity Based scheduling	
6		Real Time Operating Systems and Mobile OS	08
	6.1	Characteristics of Real Time operating Systems, Classification of Real Time Operating Systems, Scheduling in RTOS: Clock driven: cyclic, Event driven: EDF and rate monotonic scheduling.	
	6.2	Mobile OS: Architecture, Android OS, iOS, Virtual OS, Cloud OS and their design issues	

Assessment:

Internal Assessment:

Assessment consists of two class tests of 20 marks each. The first class test is to be conducted when approx. 40% syllabus is completed and second class test when additional 40% syllabus is completed. Duration of each test shall be one hour.

End Semester Theory Examination:

- Question paper will comprise of 6 questions, each carrying 20 marks.
- The students need to solve total 4 questions.
- Question No.1 will be compulsory and based on entire syllabus.
- Remaining question (Q.2 to Q.6) will be selected from all the modules.

Text Books:

1. The Design of the UNIX Operating System, PHI, by Maurice J. Bach.
2. Distributed Computing 2nd Edition, Mahajan and Seema Shah, OXford.
3. Advanced Concepts in Operating Systems, Mukesh Singhal, Niranjan G Shivaratri.
4. Mobile Computing by Rajkamal, 1st edition, Oxford.
5. Real Time Operating System, Jane W.S. Liu, Pearson.

Reference Books:

1. Andrew S. Tanenbaum and Maarten Van Steen, "Distributed Systems: Principles and Paradigms, 2nd edition, Pearson Education.
2. "Real-Time Systems: Theory and Practice", Rajib Mall, Pearson Education India, 2006.

Course Code	Course Name	Credit
CSDLO5013	**Advanced Algorithm**	**4**

Course Objectives:
1. To provide mathematical approach for Analysis of Algorithms.
2. To teach advanced data structures.
3. To solve complex problems in real life applications.

Course Outcomes: At the end of the course student will be able to
1. Describe analysis techniques for algorithms.
2. Identify appropriate data structure and design techniques for different problems
3. Identify appropriate algorithm to be applied for the various application like geometric modeling, robotics, networking, etc.
4. Appreciate the role of probability and randomization in the analysis of algorithm
5. Analyze various algorithms.
6. Differentiate polynomial and non deterministic polynomial algorithms.

Prerequisites: Data structures, Discrete mathematics and Analysis of Algorithm

Sr. No.	Module	Detailed Content	Hours
1	**Fundamental of Algorithms**	**Introduction-** **Complexity-**complexity of recursive algorithms, finding complexity by tree method, master method, proving technique (contradiction, mathematical induction). **Amortized analysis-** aggregate analysis, accounting analysis, potential analysis dynamic tables	08
2	**Probabilistic Analysis and Randomized Algorithm**	The hiring problem Indicator random variables Randomized algorithms Probabilistic analysis and further uses of indicator random variable	08
3	**Advanced Data Structure**	**Introduction to trees and heap** **Red-Black Trees:** properties of red-black trees , Operations on Red-black trees **Binomial Heaps:** Binomial trees and binomial heaps, Operation on Binomial heaps **Analysis of all above operations**	12
4	**Maximum Flow**	Flow networks , the ford Fulkerson method ,max bipartite matching , push Relabel Algorithm , The relabel to front algorithm	08

5	**Computational Geometry**	Line Segment properties, Determining whether any pair of segment intersects, finding the convex hull, Finding the closest pair of points.	08
6	**NP-Completeness And Approximation Algorithms**	NP-Completeness: NP-Completeness and reducibility, NP-Completeness proofs, NP-Complete problems-The vertex-cover problem, The travelling salesman problem	08

Text Books:

1. Thomas H. Cormen, Charles E. Leiserson, Ronald L. Rivest, Clifford Stein, "Introduction to Algorithms", PHI, India Second Edition.
2. Horowitz, Sahani and Rajsekaran, "Fundamentals of Computer Algorithms", Galgotia.
3. Harsh Bhasin, "Algorithms – Design and Analysis", Oxford, 2015.

Reference Books:

1. Rajeev Motwani, Prabhakar Raghavan, " Randomized Algorithm", Cambridge University
2. S. K. Basu, "Design Methods and Analysis of Algorithm", PHI
3. Vijay V. Vajirani, "Approximation Algorithms",Springer.

Internal Assessment:

Assessment consists of two tests out of which; one (T1) should be compulsory class test (on at least 02 Modules) and the other (T2) is either a class test or assignments on live problems or course project

Theory Examination:

1. Question paper will comprise of total six questions.
2. All question carry equal marks
3. Questions will be mixed in nature (for example supposed Q.2 has part (a) from module 3 then part (b) will be from any module other than module 3)
4. Only Four question need to be solved.

In question paper weightage of each module will be proportional to number of respective lecture hours as mentioned in the syllabus.

Lab Code	Lab Name	Credits
CSL501	**Microprocessor Lab**	**1**

Lab Objective:

1. To emphasize on use of Assembly language program.
2. To prepare students for advanced subjects like embedded system and IOT.

Lab Outcome:

1. Use appropriate instructions to program microprocessor to perform various task
2. Develop the program in assembly/ mixed language for Intel 8086 processor
3. Demonstrate the execution and debugging of assembly/ mixed language program

Description:

A microprocessor is the most important unit within a computer system. It is responsible for processing the unique set of instructions and processes. It is a controlling unit of a computer, capable of performing Arithmetic Logical Unit (ALU) operations and communicating with the other devices connected to it. Typical microprocessor operations include adding, subtracting, comparing two numbers, and fetching numbers from one area to another. These operations are the result of a set of instructions that are part of the microprocessor design. When computer is turned on, the microprocessor gets the first instruction from the basic input/output system that comes with the computer as part of its memory. After that, either the BIOS, or the operating system that BIOS loads into computer memory, or an application program provides instructions to perform.

Suggested List of Experiments:

Sr. No.	Title of Experiments
1	Use of programming tools (Debug/TASM/MASM/8086kit) to perform basic arithmetic operations on 8bit/16 bit data
2	Code conversion (Hex to BCD, BCD to Hex, ASCII to BCD, BCD to ASCII)
3	Assembly programming for 16-bit addition, subtraction, multiplication and division (menu based)
4	Assembly program based on string instructions (overlapping/ non-overlapping block transfer/ string search/ string length)
5	Assembly program to display the contents of the flag register.
6	Mixed Language program to shift a number for given number of times
7	Assembly program to find the GCD/ LCM of two numbers
8	Assembly program to sort numbers in ascending/ descending order

9	Mixed Language program to increment, decrement the size of the cursor and also to disable it.
10	Assembly program to find minimum/ maximum no. from a given array.
11	Program for device driver (printer/mouse/keyboard)
12	Program based on 32 bit architecture (e.g. Switching from real mode to protected mode using DPMI driver, 32bit multiplication)
13	Assembly program to find factorial of number using procedure
14	Program and interfacing using 8255/ 8253
15	Program and interfacing of ADC/ DAC/ Stepper motor

Term Work:

Term should consist of at least 10 experiments.
Journal must include –

- At least one experiment with use of macros/ procedures
- At least five experiments with use of DOS, BIOS interrupts
- At least two assignments

At least one experiment on hardware interfacing is desirable
The final certification and acceptance of term work ensures satisfactory performance of laboratory work and minimum marks in term work.

Term Work: 25 marks (Total) = 15 Marks (Experiments) + 5 Marks (Assignments) + 5 Marks (Theory + Practical Attendance)

Oral & Practical exam will be based on the CSL501and CSC501 syllabus.

Lab Code	Lab Name	Credits
CSL 502	**Computer Network Lab**	**1**

Lab Objective:
To practically explore OSI layers and understand the usage of simulation tools.

Lab Outcomes:
On successful completion of course learner will be able to
1. Design and setup networking environment in Linux.
2. Use Network tools and simulators such as NS2, Wireshark etc. to explore networking algorithms and protocols.
3. Implement programs using core programming APIs for understanding networking concepts.

Description
The experiments are expected to be performed in Linux environment.

Suggested List of Experiments

Sr. No	Title of Experiments
1.	Setup a network and configure IP addressing, subnetting, masking. (Eg. CISCO Packet Tracer, Student Ed.)
2.	Use basic networking commands in Linux (ping, tracert, nslookup, netstat, ARP, RARP, ip, ifconfig, dig, route)
3.	Build a simple network topology and configure it for static routing protocol using packet tracer.
4.	Perform network discovery using discovery tools (eg. mrtg)
5.	Use Wireshark to understand the operation of TCP/IP layers : ● Ethernet Layer : Frame header, Frame size etc. ● Data Link Layer : MAC address, ARP (IP and MAC address binding) ● Network Layer : IP Packet (header, fragmentation), ICMP (Query and Echo) ● Transport Layer: TCP Ports, TCP handshake segments etc. ● Application Layer: DHCP, FTP, HTTP header formats
6.	CRC/ Hamming code implementation.
7.	Stop and wait protocol/ sliding window (selective repeat / Go back N)
8.	Use simulator (Eg. NS2) to understand functioning of ALOHA, CSMA/CD.
9.	a. Set up multiple IP addresses on a single LAN. b. Using nestat and route commands of Linux, do the following:

	- View current routing table - Add and delete routes - Change default gateway c. Perform packet filtering by enabling IP forwarding using IPtables in Linux.
10.	Implementation of DVR/ LSR in NS2/(any other simulator)
11.	Socket programming using TCP or UDP
12.	Simulate congestion control (leaky bucket / token bucket).
13.	Perform File Transfer and Access using FTP
14.	Perform Remote login using Telnet server

Term Work:

Laboratory work should be based on above syllabus of suggested list having minimum 10

experiments, covering all layers.

Experiments -- (15) Marks
Assignments -- (05) Marks
Attendance (Theory + Practical) -- (05) Marks
Total --- (25) Marks

Oral & Practical exam will be based on the **above and CSC 503 : Computer Network**.

Lab Code	Lab Name	Credits
CSL503	**Database & Information System Lab**	**1**

Lab Outcome: On successful completion of course learner will be able to:
1. Design and draw ER and EER diagram for the real life problem with software tool.
2. Create and update database and tables with different DDL and DML statements.
3. Apply /Add integrity constraints and able to provide security to data.
4. Implement and execute Complex queries.
5. Apply triggers and procedures for specific module/task
6. Handle concurrent transactions and able to access data through front end (using JDBC ODBC connectivity.)

Description:
- The below suggested experiments needs to be performed by a group of **3/4 students.**
- Select any database management system and conduct all experiments based on the same topic.

Suggested List of Experiments:

Sr. No.	Title of Experiments
1	Identify the case study and detail statement of problem. Design an Entity-Relationship (ER) / Extended Entity-Relationship (EER) Model.
2	Mapping ER/EER to Relational schema model.
3	Create and populate database using Data Definition Language (DDL) and DML Commands for you're the specified System.
4	Apply Integrity Constraints for the specified system.
5	Perform Simple queries, string manipulation operations.
6	Nested queries and Complex queries
7	Perform Join operations
8	Views and Triggers
9	Functions , cursor and procedure.
10	Transaction and Concurrency control
11	Mini project- Creating a Two-tier client-server database applications using JDBC

Assignment: Perform Normalization -1NF, 2NF, 3NF

Term Work:

Laboratory work will be based on DBMS syllabus with minimum 10 experiments to be incorporated.
Experiments should be completed by students on the given time duration
Experiments -- (10) Marks
Mini Project-- (10) Marks
Attendance (Theory + Practical) -- (05) Marks
Total --- (25) Marks

Practical and Oral :

Practical and oral Exam should be conducted for the Lab, on Database Management System subject for given list of experiments .

Implementation --------------------- --(15) Marks
Oral ---(10) Marks
Total --(25) Marks

**Oral & Practical exam will be based on the above and CSC502: 'DBMS' syllabus

Text Books:
1. G. K. Gupta :"Database Management Systems", McGraw – Hill.
2. Korth, Slberchatz,Sudarshan, :"Database System Concepts", 6th Edition, McGraw – Hill
3. Elmasri and Navathe, " Fundamentals of Database Systems", 5thEdition, PEARSON
4. Peter Rob and Carlos Coronel, " Database Systems Design, Implementation and Management", Thomson Learning, 5th Edition.

Reference Books :
1. Dr. P.S. Deshpande, SQL and PL/SQL for Oracle 10g,Black Book, Dreamtech Press
2. PaulrajPonniah, " Introduction to Database Management",Wiley publication
3. Raghu Ramkrishnan and Johannes Gehrke, " Database Management Systems",TMH
4. Debabrata Sahoo "Database Management Systems" Tata McGraw Hill, Schaum's Outline

Course Code	Course Name	Credits
CSL504	**Web Design Lab**	**2**

Course objectives:

1. To design and create web pages using HTML5 and CSS3.
2. To Create web pages and provide client side validation.
3. To create dynamic web pages using server side scripting.
4. To use MVC framework for web application development.

Course outcomes: On completion of course learner will be able to:

1. Understand the core concepts and features of Web Technology
2. Design static web pages using HTML5 and CSS3
3. Apply the concept of client side validation and design dynamic web pages using JavaScript and JQuery.
4. Evaluate client and server side technologies and create Interactive web pages using PHP , AJAX with database connectivity using MySQL.
5. Understand the basics of XML, DTD and XSL and develop web pages using XML / XSLT.
6. Analyze end user requirements and Create web application using appropriate web technologies and web development framework

Prerequisite: Data Structures, Basics of Programming Languages

Module No.	Unit No.	Topics	Hrs.
1.0		**INTRODUCTION TO WWW**	**2**
	1.1	Internet Standards – Introduction to WWW – WWW Architecture – SMTP – POP3 – File Transfer Protocol	
	1.2	Overview of HTTP, HTTP request – response — Generation of dynamic web pages- W3C Validator, How web works - Setting up the environment (LAMP/XAMP/WAMP server)	
2.0		**CLIENT SIDE PROGRAMMING**	**6**
	2.1	**Markup Language (HTML):** Introduction to HTML and HTML5 - Formatting and Fonts –Commenting Code – Anchors – Backgrounds – Images – Hyperlinks	
	2.2	Lists – Tables – Frames - HTML Forms and controls.	
	2.3	**Cascading Style Sheet (CSS):** The need for CSS, Introduction to CSS 3 – Basic syntax and structure ,CSS Properties-Inline Styles – Embedding Style Sheets	
	2.4	Linking External Style Sheets – Backgrounds –Box Model(Introduction , Border Properties, Padding Properties, Margin Properties), Manipulating text - Margins and Padding - Positioning using CSS., Creating page Layout and Site Designs	
3.0		**INTRODUCTION TO JAVASCRIPT**	**6**
	3.1	Introduction - Core features - Data types and Variables - Operators, Expressions, and Statements, Functions - Objects - Array, Date and Math related Objects	
	3.2	Document Object Model - Event Handling Controlling Windows &	

		Frames and Documents Form handling and validations.	
	3.3	**Advanced JavaScript** - Browser Management and Media Management – Classes – Constructors – Object-Oriented Techniques in JavaScript	
	3.4	Object constructor and Prototyping - Sub classes and Super classes – JSON - jQuery and AJAX., Rich Internet Application with AJAX, JQuery Framework	
4.0		**SERVER SIDE PROGRAMMING**	**8**
	4.1	Introduction - Programming basics - Print/echo - Variables and constants – Strings and Arrays	
	4.2	Operators, Control structures and looping structures – Functions – Reading Data in Web Pages	
	4.3	Embedding PHP within HTML - Establishing connectivity with MySQL database, cookies, sessions and Authentication	
	4.4	AJAX with PHP - AJAX with Databases	
5.0		**XML**	**4**
	5.1	Dynamic page generation (adding interactivity, styles, using HTML, DHTML, XHTML, CSS, Java Script), XML –DTD(Document Type Definition) - XML Schema	
	5.2	XML –DTD(Document Type Definition) - XML Schema - Document Object Model - Presenting XML - Using XML Parsers: DOM and SAX,XSL-eXtensible Style sheet Language	
6.0		**WEB DEVELOPMENT FRAMEWORK**	**2**
	6.1	Introduction to Composer - MVC Architecture	
	6.2	Web Application Development using web development framework :-Introduction to Laravel, Development of Web pages using Laravel., Example web applications – Interactive websites, web based information systems , blogs, social networking sites etc.	
		Total	**28**

Text Books:

1. Ralph Moseley , M.T. Savliya ,” Developing Web Applications”, Willy India, Second Edition, ISBN: 978-81-265-3867-6
2. “Web Technology Black Book”, Dremtech Press, First Ediction, 978-7722-997
3. Robin Nixon, "Learning PHP, MySQL, JavaScript, CSS & HTML5" Third Edition, O'REILLY,2014.
    (http://www.ebooksbucket.com/uploads/itprogramming/javascript/Learning_PHP_MySQL_Javascript_CSS_HTML5__Robin_Nixon_3e.pdf)
4. Professional Rich Internet Applications: AJAX and Beyond, Dana Moore, Raymond Budd, Edward Benson, Wiley publications.
    https://ebooks-it.org/0470082801-ebook.htm

Reference Books:

1. Harvey & Paul Deitel& Associates, Harvey Deitel and Abbey Deitel, “Internet and World Wide Web - How To Program”, Fifth Edition, Pearson Education, 2011.
2. Achyut S Godbole and AtulKahate, “Web Technologies”, Second Edition, Tata McGraw Hill, 2012.
3. Thomas A Powell, Fritz Schneider, “JavaScript: The Complete Reference”, Third Edition, Tata McGraw Hill, 2013.

4. David Flanagan, "JavaScript: The Definitive Guide, Sixth Edition", O'Reilly Media, 2011
5. Steven Holzner, "The Complete Reference - PHP", Tata McGraw Hill, 2008
6. Mike Mcgrath, "PHP & MySQL in easy Steps", Tata McGraw Hill, 2012.

Digital Material:
1. www.nptelvideos.in
2. www.w3schools.com
3. http://spoken-tutorial.org

Term work Assessment:

Term work will consist of lab experiments testing all the technologies included in syllabus and a **Mini project** solving an appropriate problem using the above technology.

Module	Detailed Contents	Lab Sessions
1	Installation and Setting of LAMP / WAMP / XAMP	1
2	Create Simple web page using HTML5	1
3	Design and Implement web page using CSS3 and HTML5	1

4	Form Design and Client Side Validation using :
	    a. Javascript and HTML5
	    b. Javascript and JQuery
		2

5	Develop simple web page using PHP	1
6	Develop interactive web pages using PHP with database connectivity MYSQL	2
7	Develop XML web page using DTD, XSL	1
8	Implement a webpage using Ajax and PHP	1
9	Hosting the website with Domain Registration Process.	1
10	Design a Web application using Laravel Framework	3

* *Setting up /buying the web host management system for hosting of mini project is recommended.

Term Work: The distribution of marks for term work shall be as follows:

- Lab Assignments : 10 Marks
- Mini Project   : 10 Marks
- Attendance   : 05 Marks

Practical & Oral Examination:

Practical & Oral examination is to be conducted by pair of internal and external examiners based on the above syllabus.

Course Code	Course Name	Credits
CSL505	**Business Communication & Ethics**	**02**

Course Objectives:
1. To inculcate professional and ethical attitude at the work place
2. To enhance effective communication and interpersonal skills
3. To build multidisciplinary approach towards all life tasks
4. To hone analytical and logical skills for problem-solving.

Course Outcomes: Learner will be able to…
1. Design a technical document using precise language, suitable vocabulary and apt style.
2. Develop the life skills/interpersonal skills to progress professionally by building stronger relationships.
3. Demonstrate awareness of contemporary issues knowledge of professional and ethical responsibilities.
4. Apply the traits of a suitable candidate for a job/higher education , upon being trained in the techniques of holding a group discussion, facing interviews and writing resume/SOP.
5. Deliver formal presentations effectively implementing the verbal and non-verbal skills

Module	Detailed Contents	Hrs.
01	**Report Writing**	**05**
1.1	Objectives of Report Writing	
1.2	Language and Style in a report	
1.3	Types: Informative and Interpretative (Analytical, Survey and Feasibility) and Formats of reports(Memo, Letter, Short and Long Report)	
02	**Technical Writing**	**03**
2.1	Technical Paper Writing(IEEE Format)	
2.2	Proposal Writing	
03	**Introduction to Interpersonal Skills**	**09**
3.1	Emotional Intelligence	
3.2	Leadership and Motivation	
3.3	Team Building	
3.4	Assertiveness	
3.5	Conflict Resolution and Negotiation Skills	
3.6	Time Management	
3.7	Decision Making	
04	**Meetings and Documentation**	**02**
4.1	Strategies for conducting effective meetings	
4.2	Notice, Agenda and Minutes of a meeting	
4.3	Business meeting etiquettes	
05	**Introduction to Corporate Ethics**	**02**
5.1	Professional and work ethics (responsible use of social media Facebook, WA, Twitter etc.)	
5.2	Introduction to Intellectual Property Rights	
5.4	Ethical codes of conduct in business and corporate activities (Personal ethics, conflicting values, choosing a moral response and making ethical decisions)	

06	Employment Skills	07
6.1	Group Discussion	
6.2	Resume Writing	
6.3	Interview Skills	
6.4	Presentation Skills	
6.5	Statement of Purpose	
		28

Assessment:

List of Assignments

1. Report Writing(Theory)
2. Technical Proposal
3. Technical Paper Writing(Paraphrasing a published IEEE Technical Paper)
4. Interpersonal Skills(Group activities and Role plays)
5. Interpersonal Skills(Documentation in the form of soft copy or hard copy)
6. Meetings and Documentation(Notice, Agenda, Minutes of Mock Meetings)
7. Corporate ethics(Case studies, Role plays)
8. Writing Resume and Statement of Purpose

Term Work

Term work shall consist of all assignments from the list. The distribution of marks for term work shall be as follows:

Book Report	**10 marks**
Assignments:	**10 marks**
Project Report Presentation:	**15 marks**
Group Discussion:	**10 marks**
Attendance:	**05 marks**

References:

1. Fred Luthans," Organizational Behavior", Mc GrawHill,
2. Lesiker and Petit," Report Writing for Business ",McGrawHill
3. R. Subramaniam," Professional Ethics" Oxford University Press
4. Huckin and Olsen, "Technical Writing and Professional Communication ",McGraw
5. Raman and Sharma,FundamentalsofTechnicalCommunication,OxfordUniversityPress
6. Hill Wallace and Masters," Personal Development for Life and Work", Thomson Learning.
7. Heta Murphy," *Effective Business Communication* ",McGraw Hill, edition
8. R.C Sharma and Krishna Mohan," *Business Correspondence and Report Writing",*
9. Raman Sharma, *"Communication Skills"*, Oxford University Press
10. B N Ghosh," *Managing Soft Skills for Personality Development* ",Tata McGraw Hill
11. Dufrene, Sinha,"*BCOM*", Cengage Learning, 2ndedition
12. Bell. Smith,"ManagementCommunication"WileyIndiaEdition,3rdedition.
13. Dr. K. Alex, "Soft Skills", S Chand and Company
14. Robbins Stephens P., "Organizational Behavior", Pearson Education
15. https://grad.ucla.edu/asis/agep/advsopstem.pdf